



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): De-Wei LEE, et al.

Serial No.: 10/756,879

Filed: January 13, 2004

Title: "METHOD AND SYSTEM FOR
SYNCHRONIZING ALL CLOCK SOURCES OF
SEMICONDUCTOR DEVICES"

) Re: Comments on Notice of Allowability
)
) Notice of Allowance dated: December 26, 2006
)
) Confirmation No.: 3435
)
) Examiner: Suresh SURYAWANSHI
)
) Art Unit: 2115
)
) Our Ref No.: B-5353 621646-3
)
) Date: March 7, 2007

COMMENTS ON NOTICE OF ALLOWABILITY

MAIL STOP: ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In the Notice of Allowance and Fee(s) Due dated December 26, 2006, the title of the application is incorrect.

The title on the PTOL-85 form reads "Method and System for Synchronization all Clock Sources of Semiconductor Devices".

The correct title, in accordance to Examiner Suresh Suryawanshi's December 13, 2006 amendment (Page 2 of the Notice of Allowability), should read "Method and System for Synchronizing all Clock Sources of Semiconductor Devices".

Please ensure that the Patent issues with the correct title.

Respectfully submitted,

Robert Popa

Reg. No. 43,010

LADAS & PARRY

5670 Wilshire Blvd, Ste. 2100

Los Angeles, California 90036

(323) 934-2300

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to: MAIL STOP ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 7, 2007.

By: _____

Linda S. Santiago